PAGE 9112 * RCVD AT 312812005 12:54:54 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-113 * DNIS:8729306 * CSID:845 892 6363 * DURATION (mm-55):03-30

I hereby certify that this correspondence is being deposited via facsimile to : Commissioner of Patents and Trademarks, Alexandria, VA 22313, on March 28, 2005. The applicant and/or attorney requests the date of deposit as the filing date.

Depositor: Robert Faber

28-05 (Signature & date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re application of

March 28, 2005

Doris, et al.

Group Art Unit: 2818

Serial No: 10/604,190

Examiner: Mai Huong C. Train

Filed: 6/30/03

International Business Machines Corporation

2070 Route 52

Hopewell Junction, NY 12533

TITLE: High Performance CMOS Device Structures and Method of Manufacture

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the US and non-US patents and publications as listed on Form PTO-1449, attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or nonpertinency of the art, that better art than that listed is not available, or that other art is not applicable.

No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,

Doris, et al.

Joseph P Registration No. 39,238

Telephone No. 845-894-4633

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	<u> </u>	6,825,529	11-30-04	Chidambarrao, et al.		.257	336	12/12/02	
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